



### Schematic Symbol



### Description

SPxxxxSB series is a type of semiconductor protection thyristor with surface mount package solution. It is designed to protect baseband equipment from damaging overvoltage transients, such as modems, telephones, line cards, answering machines, FAX machines, T1/E1, xDSL and more.

### Features

- Lower capacitance
- Low profile package
- Low on-state voltage
- Max Surge capability 10×700 μS @40 Ω: 4 kV
- Excellent capability of absorbing transient surge
- Quick response to surge voltage (ns Level)
- Eliminates overvoltage caused by fast rising transients
- Moisture sensitivity level: Level 1
- Non degenerative
- Flammability Rating: UL 94 V-0
- Halogen free and RoHS compliant

### Order Information (Example)

Type	Package	Marking Code	Delivery Form	Delivery Quantity
SP2300SB	DO-214AA(SMB)	P23B	13" T&R	3000 PCS

### Limiting Values

(T<sub>A</sub> = 25 °C, unless otherwise specified)

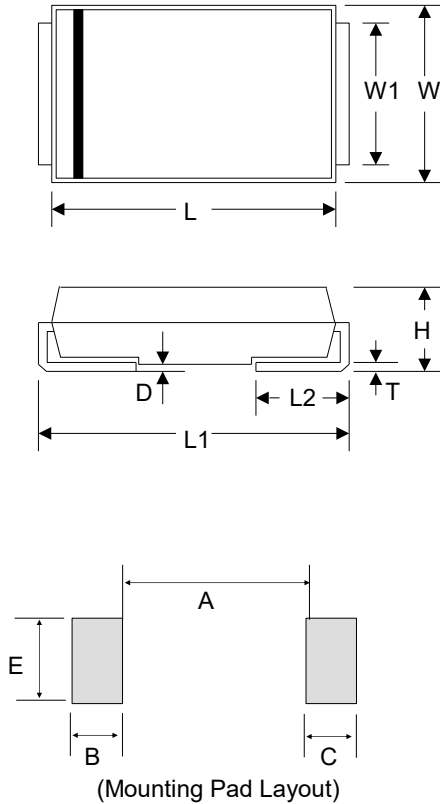
Symbol	Parameter	Conditions	Min	Max	Unit
I <sub>PP</sub>	Repetitive peak pulse current	10 / 1000 μs	80	-	A
T <sub>J</sub>	Operating Temperature Range	-	-40	150	°C
T <sub>stg</sub>	Storage Temperature Range	-	-55	150	°C

Surge Rating	I <sub>PP</sub> (A) min				
	2 / 10 μs <sup>1</sup>	8 / 20 μs <sup>1</sup>	10 / 360 μs <sup>1</sup>	10 / 700 μs <sup>2</sup>	10 / 1000 μs <sup>1</sup>
B	250	250	125	100	80

Notes:

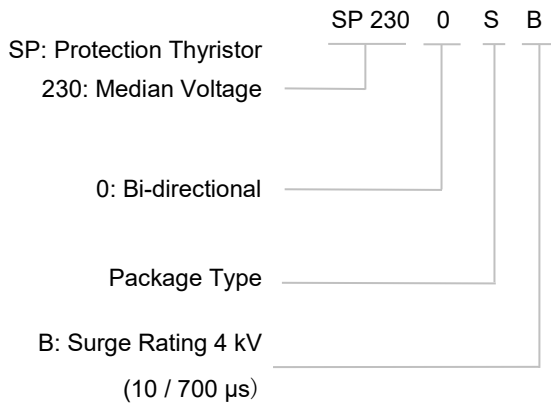
1. Current waveform in μs<sup>1</sup>.
2. Voltage waveform in μs<sup>2</sup>.

### Package Dimensions

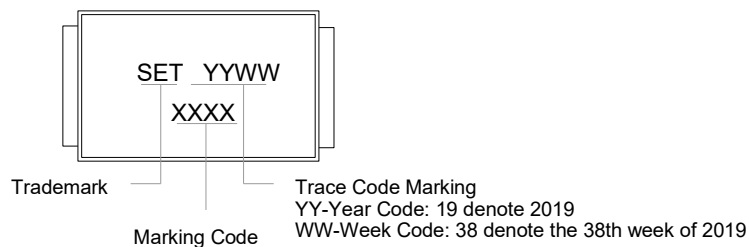


Symbol	Millimeters		Inches	
	Min.	Max.	Min.	Max.
L	4.06	4.75	0.160	0.187
W	3.30	3.94	0.130	0.155
W1	1.93	2.20	0.076	0.086
H	1.99	2.61	0.078	0.103
T	0.152	0.305	0.006	0.012
L1	5.21	5.59	0.205	0.220
L2	0.76	1.52	0.030	0.060
D	-	0.203	-	0.008
A	-	2.74	-	0.107
B	2.16	-	0.085	-
C	2.16	-	0.085	-
E	2.26	-	0.089	-

### Part Numbering System (Example)



### Marking



### Electrical Characteristics

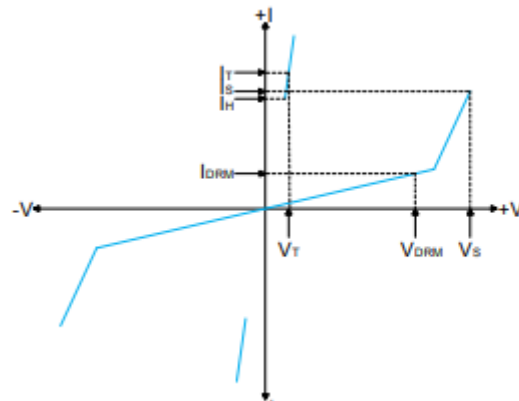
(T<sub>A</sub> = 25 °C, unless otherwise specified)

Part Number	I <sub>DRM</sub> @V <sub>DRM</sub>		V <sub>S</sub> <sup>1</sup> @I <sub>S</sub>		V <sub>T</sub> @I <sub>T</sub>		I <sub>H</sub>	Capacitance <sup>2</sup>	Marking Code
	μA	V	V	mA	V	A	mA	pF	
	max		max	max	max	max	min	max	
SP0080SB	1	6	15	800	4	2.2	50	30	P8BC
SP0220SB	1	18	30	800	4	2.2	30	50	P22B
SP0300SB	1	25	40	800	4	2.2	30	50	P03B
SP0640SB	1	58	77	800	4	2.2	120	40	P06B
SP0720SB	1	66	87	800	4	2.2	120	40	P07B
SP0900SB	1	75	98	800	4	2.2	120	40	P09B
SP1100SB	1	90	130	800	4	2.2	120	35	P11B
SP1300SB	1	120	160	800	4	2.2	120	35	P13B
SP1500SB	1	140	180	800	4	2.2	120	35	P15B
SP1800SB	1	170	220	800	4	2.2	120	35	P18B
SP2300SB	1	190	260	800	4	2.2	120	30	P23B
SP2600SB	1	220	300	800	4	2.2	120	30	P26B
SP3100SB	1	275	350	800	4	2.2	120	25	P31B
SP3500SB	1	320	400	800	4	2.2	120	25	P35B
SP3800SB	1	340	450	800	4	2.2	120	25	P38B
SP4200SB	1	400	520	800	4	2.2	100	35	P42B

Notes:

1. V<sub>S</sub> is measured at 100 kV / S.
2. Off-state capacitance is measured in VDC=2 V, VRMS=1 V, f=1 MHz.

Symbol	Parameter
V <sub>DRM</sub>	Peak off-state voltage
I <sub>DRM</sub>	Off-state current
V <sub>S</sub>	Switching voltage
I <sub>S</sub>	Switching current
V <sub>T</sub>	On-state voltage
I <sub>T</sub>	On-state current
I <sub>H</sub>	Holding current
C <sub>O</sub>	Off-state capacitance



### Performance Curve for Reference

( $T_A=25\text{ }^\circ\text{C}$  unless otherwise noted)

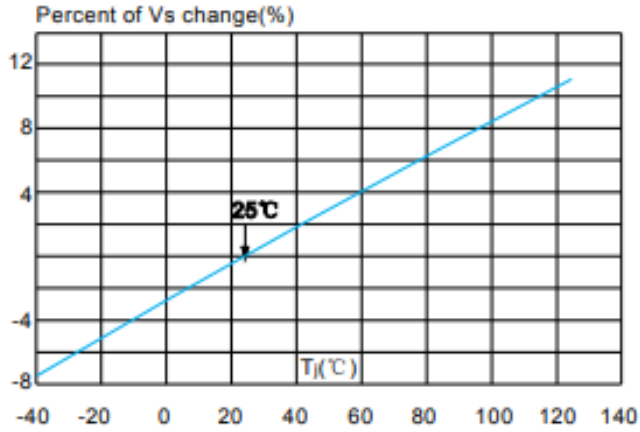


FIGURE 1

Normalized VS. Change VS. Junction Temperature

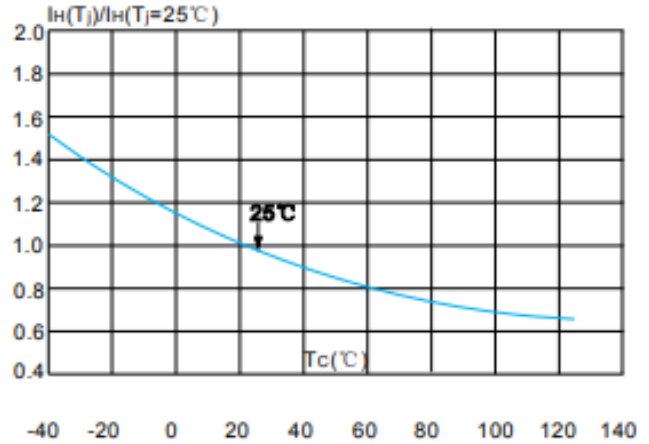


FIGURE 2

Normalized DC Holding Current VS. Case Temperature

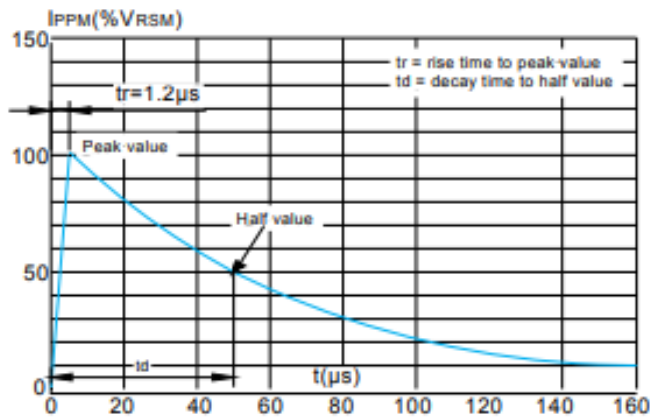


FIGURE 3

$t_r \times t_d$  Pulse Waveform

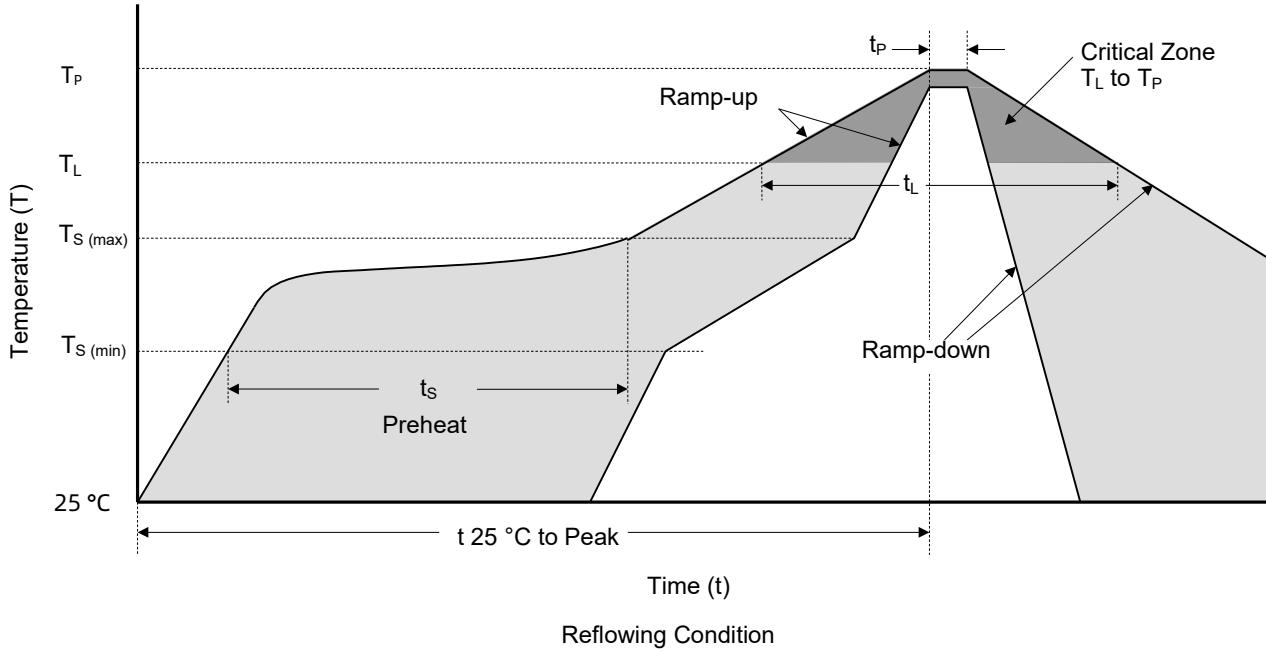
### Environmental Specifications

High Temp. Storage	JESD22-A103
HTRB	JESD22-A108
Temperature Cycling	JESD22-A104
MSL	JESDEC-J-STD-020, Level 1
H3TRB	JESD22-A101
RSH	JESD22-A111

### Physical Specifications

Weight	0.003 ounce, 0.093 grams
Case	JESD22DO214AA. Molded plastic body over glass passivated junction
Polarity	Color band denotes positive end (cathode) except Bidirectional
Terminal	Matte Tin-plated leads, Solderability per JESD22-B102

### Soldering Parameters



Reflow Soldering Parameters		Lead-Free Assembly
Pre-heat	Temperature Min ( $T_{S (min)}$ )	150 °C
	Temperature Max ( $T_{S (max)}$ )	200 °C
	Time (min to max) ( $t_s$ )	60 ~ 120 seconds
Average Ramp Up Rate ( Liquidus Temp ( $T_L$ ) to Peak Temp ( $T_P$ ))		3 °C / second max.
$T_S (max)$ to $T_L$ -Ramp-up Rate		3 °C / second max.
Reflow	Temperature ( $T_L$ )	217 °C
	Time ( $t_L$ )	60 ~ 150 seconds
Peak Temperature ( $T_P$ )		260 <sup>+0/-5</sup> °C
Time of within 5 °C of Actual Peak Temperature ( $t_p$ )		30 seconds
Ramp-down Rate		6 °C / second max.
Time From 25 °C to Peak Temperature		8 Minutes max.
Do Not Exceed		260 °C

### Packaging Information

Tape	Symbol	Dimension (mm)
	W	12.00+0.30/-0.10
	P <sub>0</sub>	4.00±0.10
	P <sub>1</sub>	8.00±0.10
	P <sub>2</sub>	2.00±0.05
	D <sub>0</sub>	1.55±0.05
	D <sub>1</sub>	1.55±0.05
	E	1.75±0.1
	F	5.50±0.05
	A <sub>0</sub>	3.78±0.10
	B <sub>0</sub>	5.65±0.15
	K <sub>0</sub>	2.70±0.10
	T	0.30±0.05

Reel Size	Symbol	Dimension (mm)
	A	330
	C	13.2
	W <sub>1</sub>	12.5

Part Number	Package	QTY's (Reel)	Packaging Option	Packaging Specification
SPxxxxSB	DO-214AA	3000 PCS	Tape & Reel – 12 mm tape / 13" reel	EIA STD RS-481



# ATTENTION

## Usage

1. TSS must be operated in the specified ambient temp..
2. Do not clean the TSS with strong polar solvent such as ketone, esters, benzene and halogenated hydrocarbon, to avoid damaging the encapsulating layer.
3. Please do not apply severe vibration, shock or pressure to TSS, to avoid element cracking.

## Replacement

1. If TSS is visually damaged, please replace it.
2. TSS is a non-repairable product. For safety sake, please use equivalent TSS for replacement.

## Storage

1. Storage Temp. Range: (-55 to 150) °C.
2. Do not store the TSS at the high temp., high humidity or corrosive gas environment, to avoid influencing the solder- ability of the lead wires. The product shall be used up within 1 year after receiving the goods.

## Environmental Conditions

1. TSS should not be exposed to the open air, nor direct sunshine.
2. TSS should avoid rain, water vapor or other condition of high temp. and high humidity.
3. TSS should avoid sand dust, salt mist, or other harmful gases.

## Max. Typical Capacitance of TSS

The typical capacitance of TSS is listed in the specifications. Designers may refer to it when designing TSS in High frequency circuit.

## Installation Mechanical Stress

1. Do not knock TSS when installing, to avoid mechanical damage.
2. Please do not apply severe vibration, shock or pressure to TSS, to avoid surface resin or element cracking.

Thyristor Surge Suppressors (Surface Mount) Feature Overview

Package Type	Series								
	SP0080TALC	SP0080TBLC	SPxxxxSA	SPxxxxSB	SPxxxxSC	SPxxxxSD	SPxxxxSDT	SP0080SDT-3L	SIP5024G
DO-214AA									
DO-214AB									
DO-214AC									
SOD-23-6									
Product Outline (mm)									
Surge Rating	A (2 kV @ 10/700 μs)	B (4 kV @ 10/700 μs)	B (4 kV @ 10/700 μs)	B (4 kV @ 10/700 μs)	C (6 kV @ 10/700 μs)	D (8 kV @ 10/700 μs)	DT (6 kV@ 1.2/50 μs)	DT (6 kV@ 1.2/50 μs)	NA
V <sub>DRM</sub> (V) Peak off-state voltage	6	6	58 ~ 340	6 ~ 400	6 ~ 440	6 ~ 400	6, 15, 400	6	24
I <sub>PP</sub> (A) (8/20 μs) Repetitive peak pulse current	90	250	250	250	400	700	3000	3000	35
C <sub>J</sub> (pF) capacitance	10	30	20 ~ 35	25 ~ 50	35 ~ 60	40 ~ 150	500, 250, 170	700	11
Operating Temperature (°C)	-55 to +150								